

Fig. 1

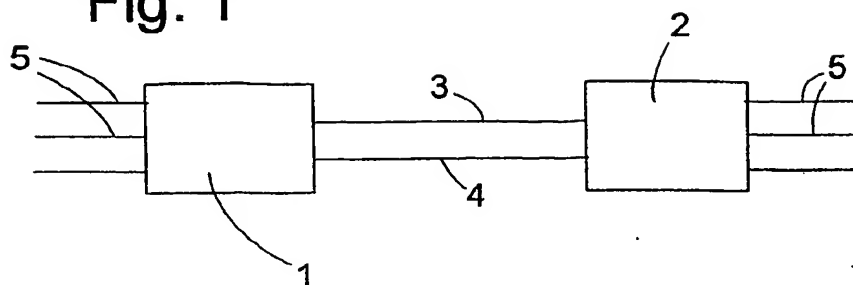
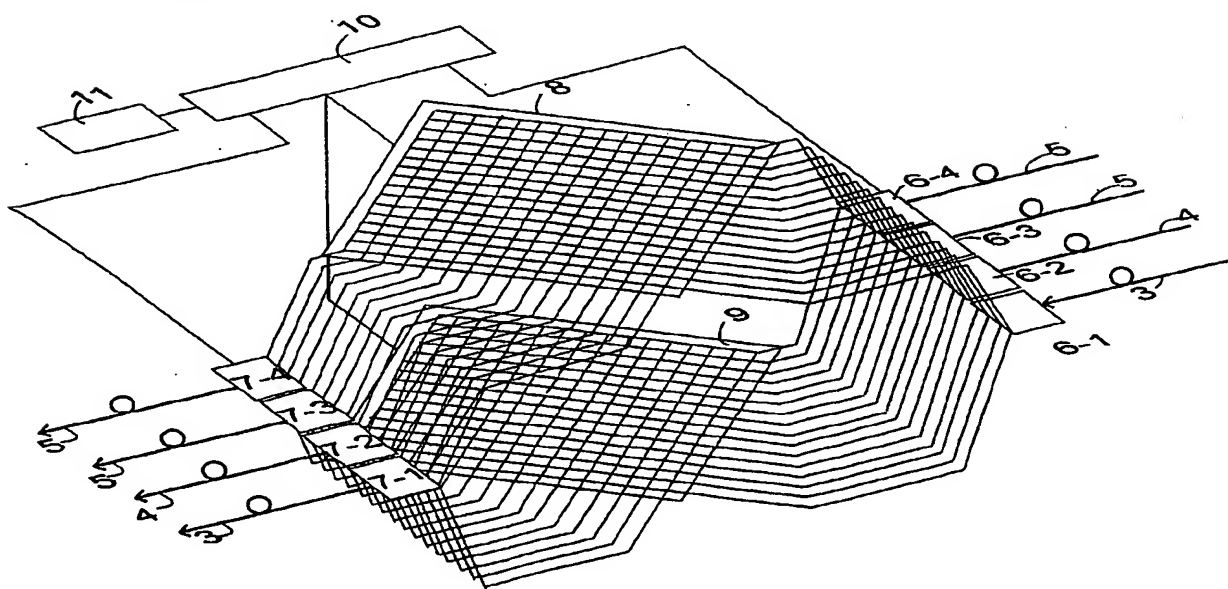


Fig. 2



A block diagram showing a small rectangular block labeled '11' connected by a horizontal line to a larger rectangular block labeled '10'. Block 10 has three short vertical lines extending downwards from its bottom edge, representing a multi-bit output bus.

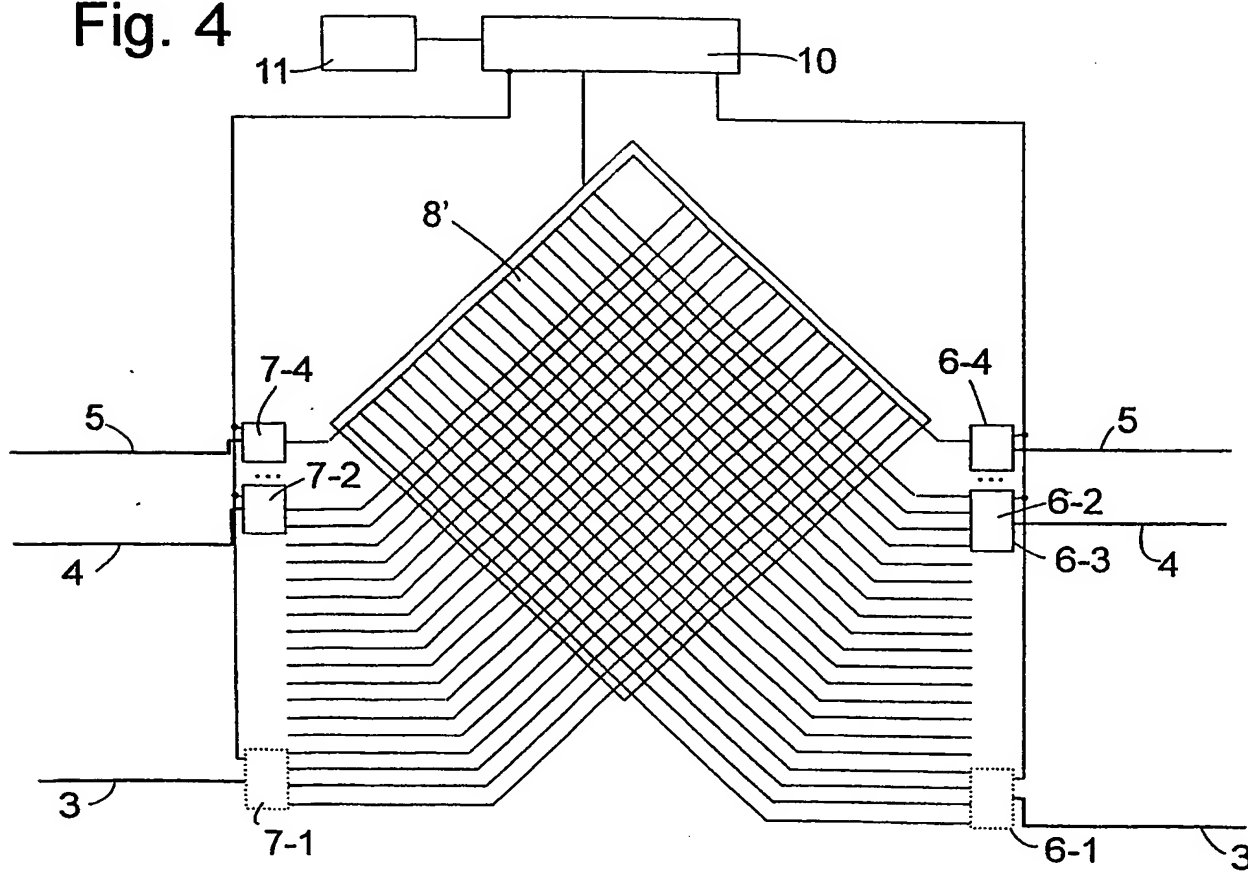


Fig. 5

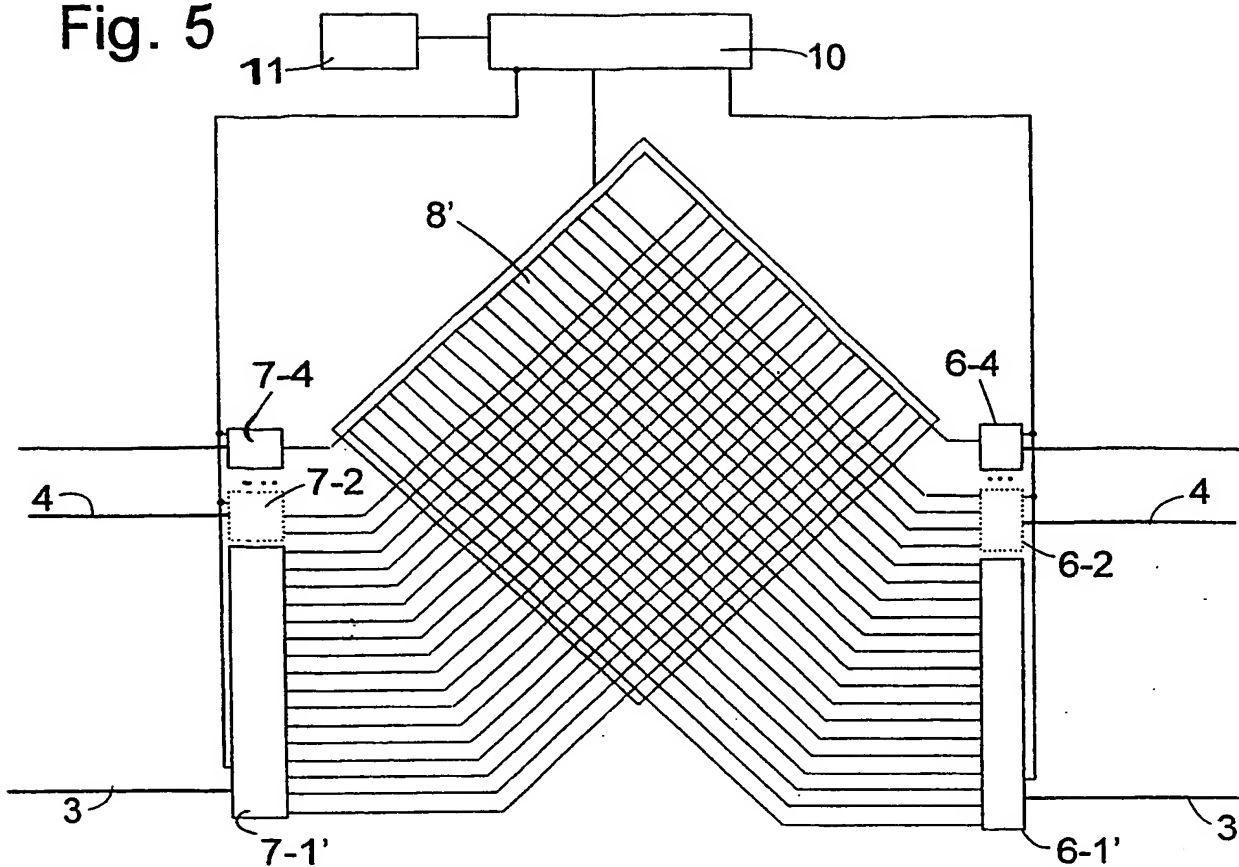


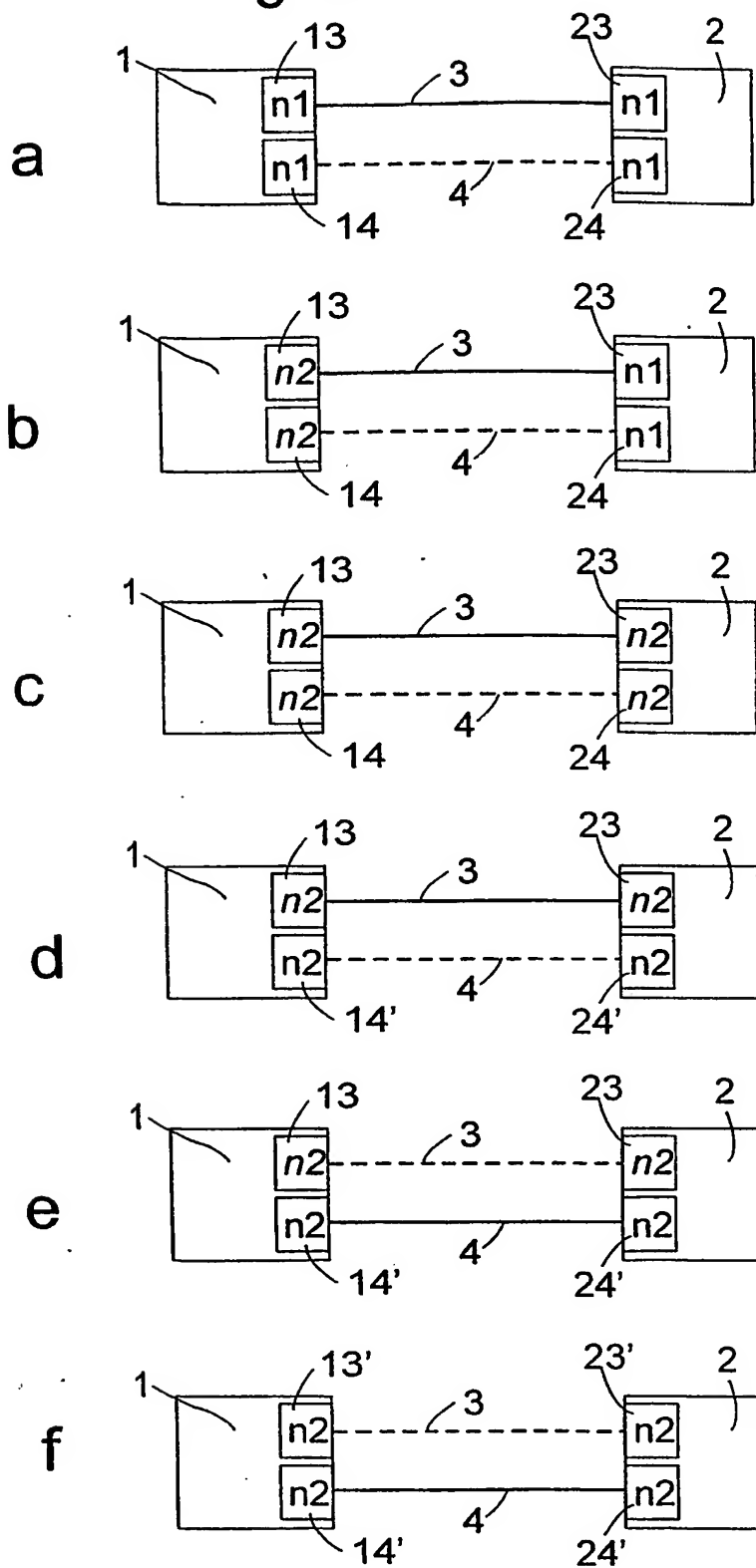
Fig. 6

Fig. 7

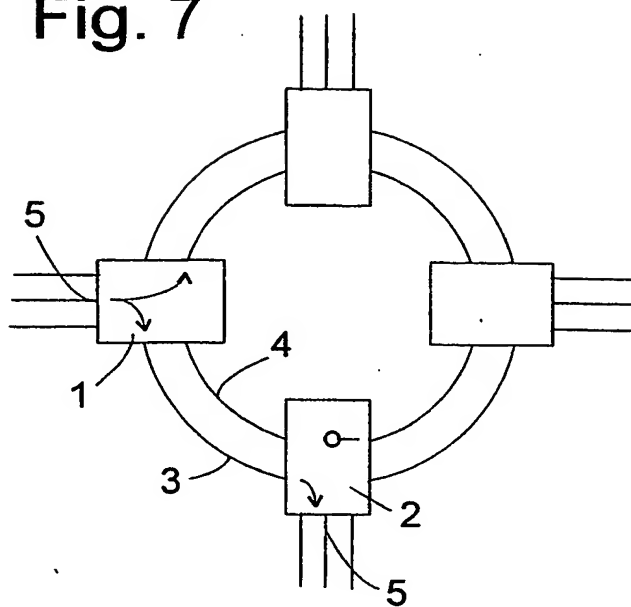


Fig. 8

